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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/814,472	03/29/2004	Paul L. Corredoura	10021250-1	8099

7590 04/25/2007
AGILENT TECHNOLOGIES, INC.
Legal Department, DL 429
Intellectual Property Administration
P.O. Box 7599
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EXAMINER	
BAYARD, EMMANUEL	
ART UNIT	PAPER NUMBER
2611	

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	04/25/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary	Application No.	Applicant(s)
	10/814,472	CORREDOURA ET AL.
	Examiner	Art Unit
	Emmanuel Bayard	2611

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 29 March 2004.
 2a) This action is FINAL. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-6 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 1-6 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)	5) <input type="checkbox"/> Notice of Informal Patent Application
Paper No(s)/Mail Date _____.	6) <input type="checkbox"/> Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

2. Claims 1-6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kumar et al U.S. Pub No 2003/0076899 A1 in view of Kodama et al U.S. Patent No 7,023,324.

As per claim 1, Kumar et al teaches an upconverting circuit comprising: a clock for defining a sequence of input polyphase cycles (see fig.1 element 40); a polyphase component generator that provides N.sub.p polyphase components at each input polyphase cycle, wherein N.sub.p>2 (see fig.1 element 38 and page 6, paragraph [0044]); a buffer is the same as the claimed (memory) that stores said polyphase components from at least one polyphase cycle prior to the current polyphase cycle (see page 7, paragraph [0048]); a plurality of filters, each filter processing a plurality of said polyphase components stored in said memory to generate a filtered polyphase component corresponding to that filter (see fig.1 elements 42a-42m and page 6 [0046] and page 7 [0048]).

However Kumar et al does not teach a multiplexer that outputs said filtered polyphase components in a predetermined order to generate a filtered output signal.

Kodama et al teaches a multiplexer that outputs said filtered polyphase components in a predetermined order to generate a filtered output signal (see fig.13a col.17, lines 29-34).

It would have been obvious to one of ordinary skill in the art to implement the teaching of Kodama et al into Kumar as to select the sub-carrier in such a manner that the frequency position where the large noise component would be present may be previously avoided so that the communication having higher reliability could be realized as taught by Kodama (see col.21, lines 37-40).

As per claim 2, Kumar does teach wherein each filter utilizes the same functional relationship to generate said filtered polyphase components (see page 4 [0033]).

As per claim 3, Kumar teaches a Buffer (memory) (see page 7, paragraph [0048]). Furthermore implementing a shift register in the (memory) buffer of Kumar would have been obvious to one skilled in the art as to hold the binary word representing the sampled value for the time duration.

As per claim 4, Kumar teaches wherein said filters are finite impulse response filters (see page 6 [0045]).

As per claim 5, Kumar teaches wherein said filters generate a filtered polyphase component that depends on a non-linear combination of said polyphase components (see page 4 [0033]).

As per claim 5, Kumar teaches wherein said polyphase component generator

receives one pair of digital signals in each polyphase cycle (see fig.1 elements 26 and 28).

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Marshall, Jr U.S. Patent No 4,393,456 teaches a digital filter bank.

Fields et al U.S. Patent No 6,085,077 teaches hardware efficient digital channelized receiver.

Ko U.S. patent No 5,917,735 teaches apparatus and method for compensating for timing and phase error.

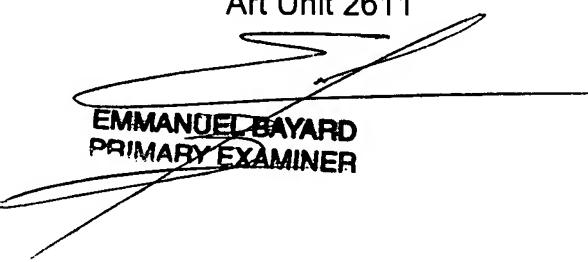
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Emmanuel Bayard whose telephone number is 571 272 3016. The examiner can normally be reached on Monday-Friday (7:Am-4:30PM) Alternate Friday off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on 571 272 2988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Emmanuel Bayard
Primary Examiner
Art Unit 2611

4/18/07



EMMANUEL BAYARD
PRIMARY EXAMINER